

JUN 03 2004



FAX TRANSMITTAL SHEET

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5 Number of Pages (including this page)

Date: June 3, 2004
To: Annette M. Thompson
Location: United States Patent and Trademark Office
Fax No.: (703) 872-9306
From: Joanna G. Chiu - 43,629
Subject: Application No: 10/025,289
Attny Docket No. SC11522TS - Martin

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6-3-04
Date

[Signature]
Pat Thomas

MESSAGE:

Enclosed herewith is a Notice of Appeal for the above-referenced patent application.

ALL ITEMS MARKED WITH AN "X" ARE INCLUDED IN THE FAX

1.	x	1 page Fax cover sheet
2.	X	1 page Notice of Appeal (in duplicate)
3.	X	1 page Fee Transmittal (in duplicate)

PLEASE GIVE THESE PAPERS TO:

EXAMINER: Annette M. Thompson
GROUP ART UNIT 2825
SERIAL NO.: 10/025,289
FILED: December 19, 2001
INVENTOR: Andrew K. Martin et al.

PATENT
PTO/SB/31

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

APPLICANT(S): Andrew K. Martin et al. GROUP ART UNIT: 2825
APPLN. NO.: 10/025,289 EXAMINER: Annette m. Thompson
FILED: December 19, 2001
TITLE: DESIGN VERIFICATION SYSTEM FOR AVOIDING FALSE FAILURES AND
METHOD THEREFOR

Certificate of Transmission under 37 CFR 1.8

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Pat Thomas

**NOTICE OF APPEAL FROM THE EXAMINER TO THE
BOARD OF PATENT APPEALS AND INTERFERENCES**

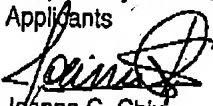
Commissioner for Patents
Alexandria, VA 22313

SIR:

In accordance with 37 CFR 1.191, applicant hereby appeals to the Board of Patent Appeals and Interferences from the last decision of the examiner. The Commissioner is hereby authorized to charge any fees which may be required, or credit any overpayment to Deposit Account No. 503079, Freescale Semiconductor, Inc. This Notice is submitted in duplicate.

FREESCALE SEMICONDUCTOR, INC.
A Motorola Subsidiary
Customer Number: 23125

Respectfully submitted,
Applicants


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